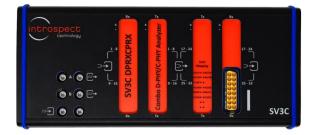


PRODUCT BRIEF

SV3C-DPRXCPRX Combo MIPI D-PHY/C-PHY Analyzer



All-in-One Protocol Analyzer for Camera and Display Interface Links

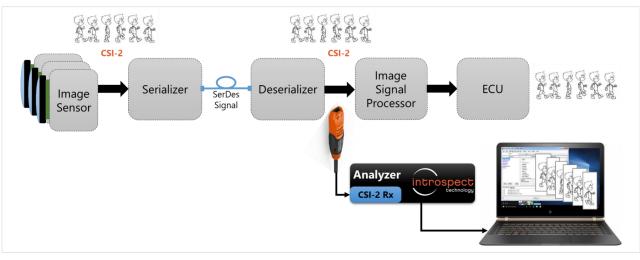
The SV3C-DPRXCPRX Combo MIPI D-PHY/C-PHY Analyzer is an ultra-portable, high-performance instrument for testing and validating MIPI-based transmitters as well as probing live systems. The SV3C-DPRXCPRX is ideal for the capture and analysis of MIPI transmitters used in cameras, displays, and other devices. It includes integrated LP and HS receivers, dynamic termination, and a range of sophisticated features including bus turn-around (BTA) and compression picture parameter set (PPS) handling. The SV3C-DPRXCPRX is ideal for both MIPI physical layer transmitter port testing and full protocol layer testing.

KEY FEATURES:

- Wide data rate support: fully continuous range from 80 Mbps to 3.5 Gbps (D-PHY) / 3.5 Gsps (C-PHY)
- **High resolution timing analysis:** physical layer and protocol layer events are recorded and analyzed for conformance
- Waveform measurement and analysis: verify signal integrity within a live in-system environment
- Hardware CRC and image checking: unlimited counting durations

KEY BENEFITS:

- **Future proof:** wide data rate coverage ensures that the same instrument can cover many product variants and design generations
- **Self-contained:** an all-in-one system reduces bench space and helps create a portable test and measurement environment; the SV3C integrates multiple tools into one
- **Automated:** scripting capability is ideal for debug tasks, physical-layer and protocol-layer conformance testing, and full-fledged production screening of devices and system modules.



Typical Application: Automotive Camera System Test and Debug



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Transmitter Parameters

Receiver Parameter	Description		Benefit
	D-PHY	С-РНҮ	benefit
Number of Differential Receivers	4 Data + 1 Clock	4 Trios	Flexible configuration and coverage for multi- lane applications
HS Differential Detectable / Allowable Voltage Swing	90 – 600 mV	90 – 500 mV	Coverage for D-PHY and C-PHY Transmitter CTS specifications
LP Differential Programmable Threshold Voltage Swing	-100 – 1500 mV	-100 – 1500 mV	Coverage for D-PHY and C-PHY Transmitter CTS specifications
Total Memory Space	4 GByte	4 GByte	Deep vector memory allows for tracing and debugging long-term events

Environment and Control

Feature	Description	Benefit
DUT Control Interface	l2C master, software programmable input/output flag pins	Enable sophisticated automation setups and camera test stations; enable output flag programming based on arbitrary trigger events
User Interface	Introspect ESP GUI allows for interoperation with embedded instruments, FPGA instruments, and other lab tools	Enables full lab automation; provides a scalable, future-proof solution
Scripting	Data logging; automatic report generation	Suited for performing full conformance testing



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Detailed Analysis Capability

